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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
Fernando Gonzalez et al.

Serial No.: 10/751,141

Filed: December 31, 2003

For: Transistor Having Vertical Junction
Edge and Method of Manufacturing
the Same

§ Group Art Unit: 2815
§
§
§ Examiner: Nguyen, Joseph H.
§
§ Atty. Docket: MICS:0114
§ 02-1010
§

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313

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Kerr Hyland Kerr Hyland	

RESPONSE TO FINAL OFFICE ACTION

In response to the Final Official Action mailed on July 14, 2005, Applicants respectfully request reconsideration of the above-identified application in view of the remarks set forth below.

OIC T Enth
JW 11/19/07